

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Samuel BOUTIN, et al.

SERIAL NO: 10/532,089

GAU: 2128

FILED: February 21, 2006

EXAMINER: PATEL, S.K.

FOR: METHOD AND DEVICE FOR SYNTHESISING AN ELECTRICAL
ARCHITECTURE

**REQUEST FOR EXTENSION OF TIME
UNDER 37 C.F.R. 1.136**

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

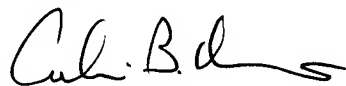
It is hereby requested that a **one** month extension of time be granted to November 1, 2009 for

- ☒ filing a response to the Official Action dated: July 1, 2009
- ☐ responding to the requirements in the Notice of Allowability dated:
- ☐ filing the Formal Drawings. The Issue Fee due has been timely filed.
- ☐ responding to the Notice to File Missing Parts of Application dated:
- ☐ filing a Notice of Appeal. A timely response to the final rejection, due has been filed.
- ☐ filing an Appeal Brief. A Notice of Appeal was filed on:
- ☐ Applicant claims small entity status. See 37 CFR 1.27.

The required fee of \$130.00 is being made by credit card payment online (if electronically filed), or is attached hereto (if paper filed), and any further charges may be made against the Attorney of Record's Deposit Account No. 15-0030.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
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